

FIG. 1A (Prior Art)

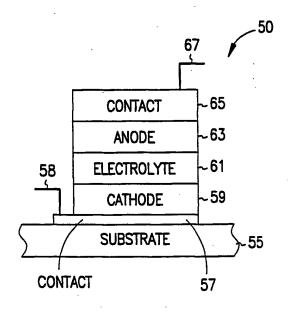
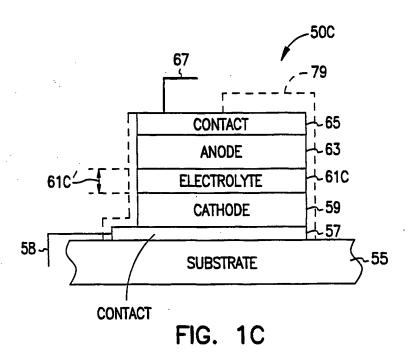
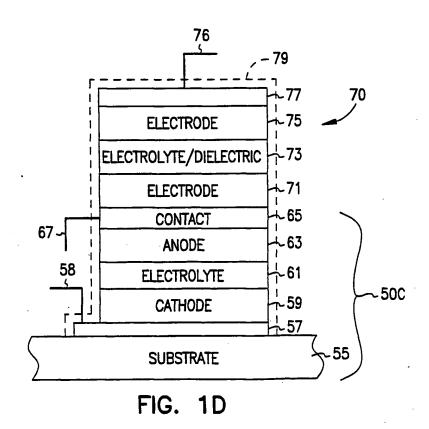


FIG. 1B









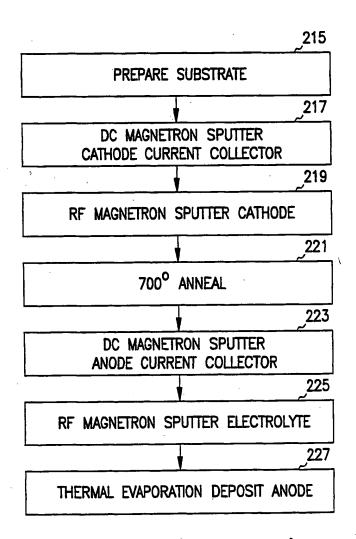


FIG. 2A (Prior Art)



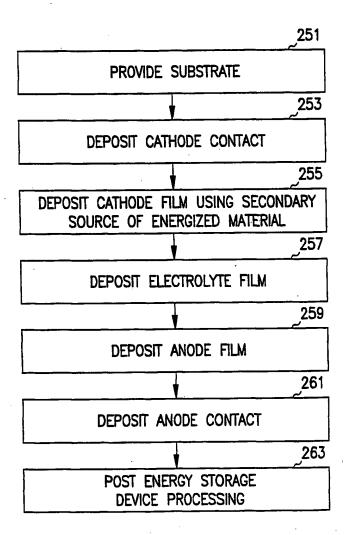


FIG. 2B



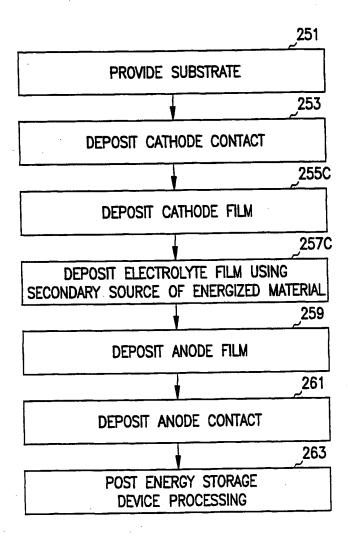


FIG. 2C



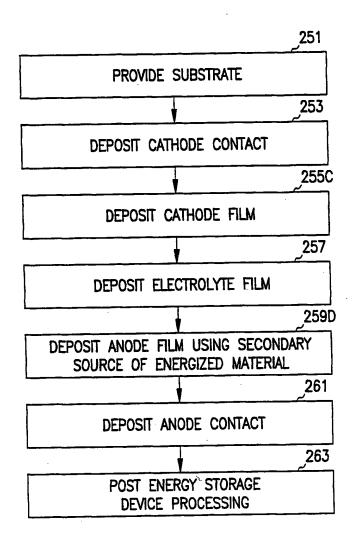


FIG. 2D



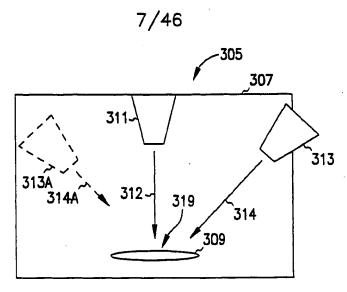


FIG. 3A

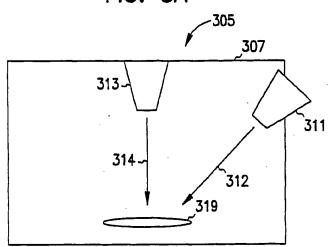


FIG. 3B

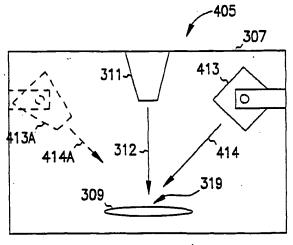


FIG. 4



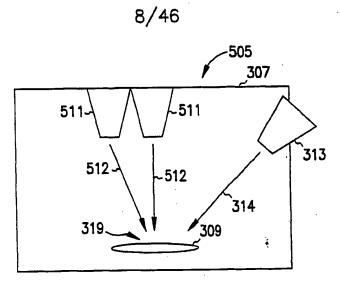


FIG. 5A

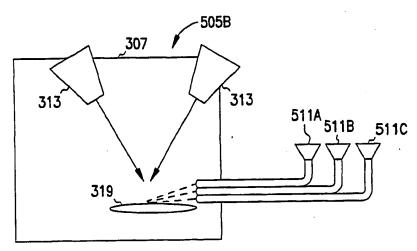


FIG. 5B

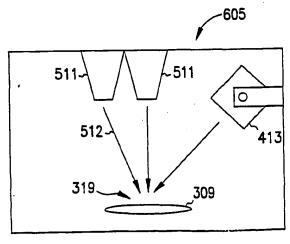


FIG. 6



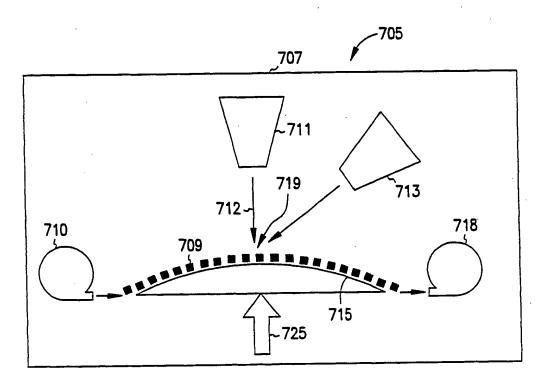
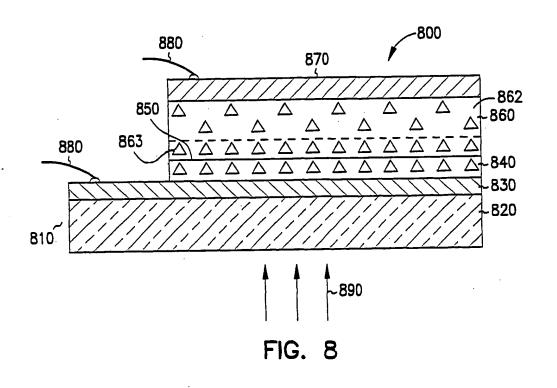
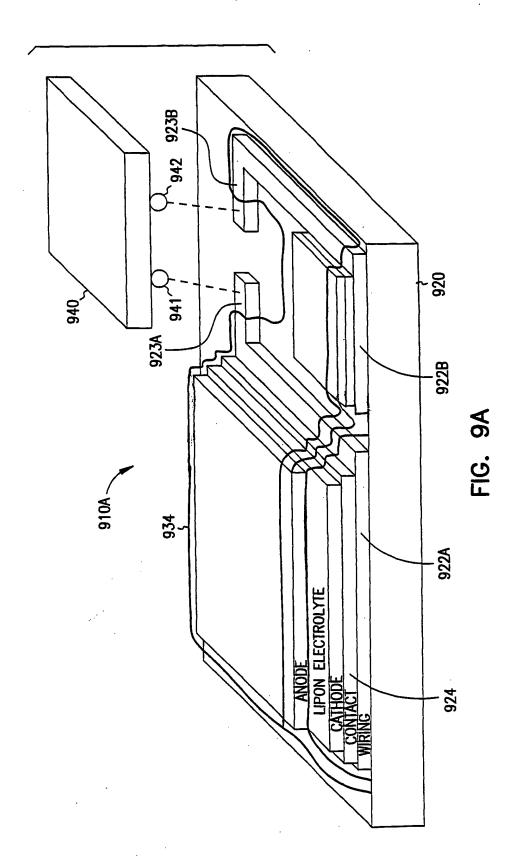


FIG. 7



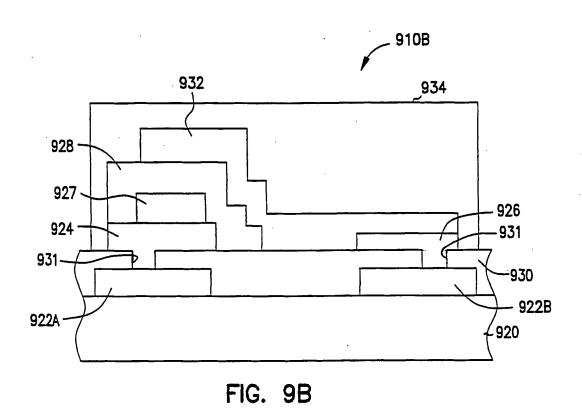


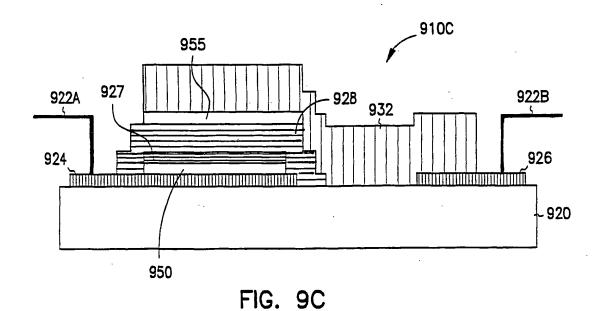
















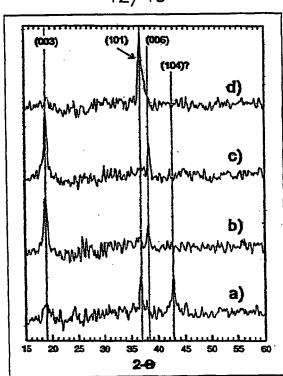


FIG. 10

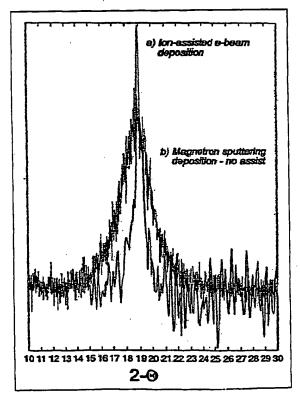


FIG. 11



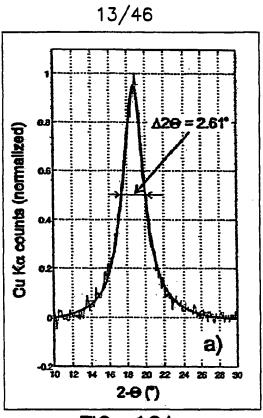


FIG. 12A

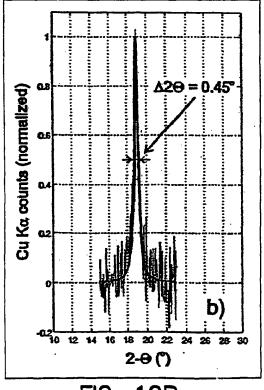
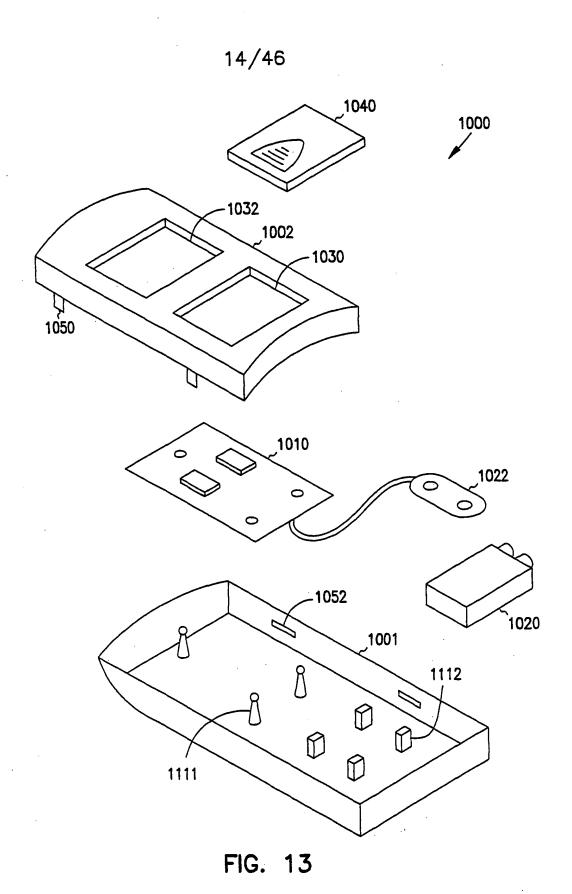


FIG. 12B



**1**".





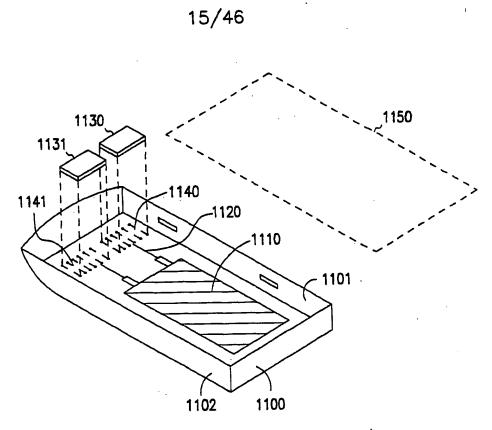


FIG. 14A

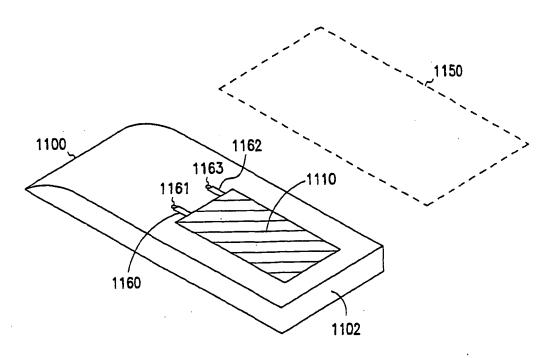


FIG. 14B



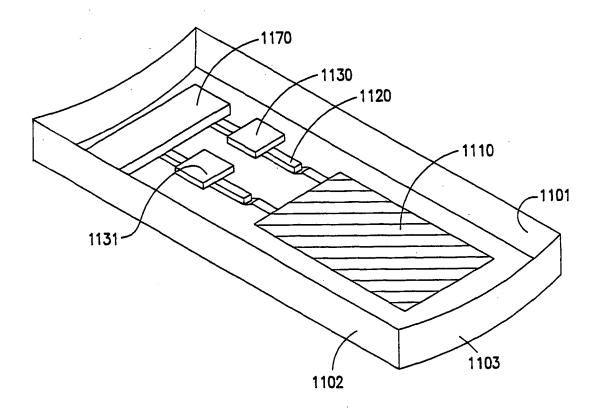
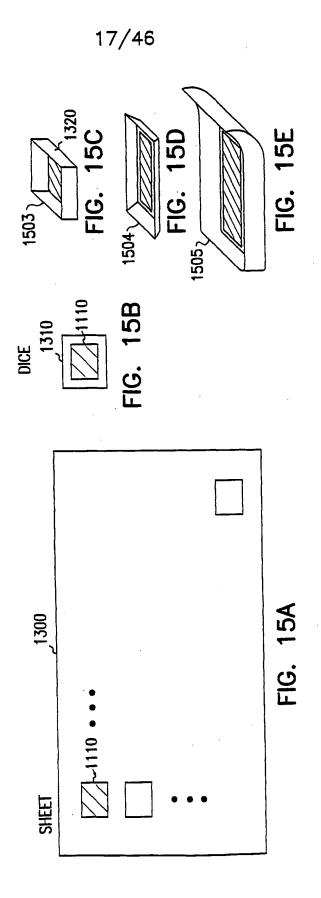
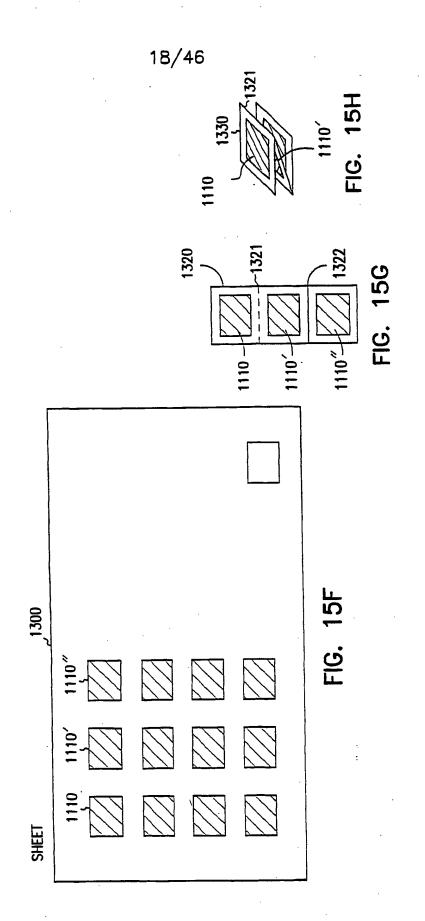


FIG. 14C











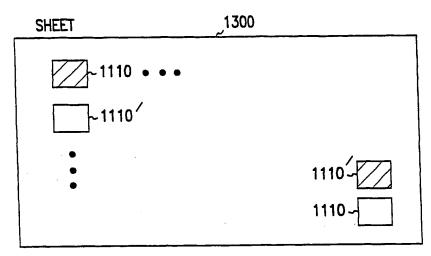
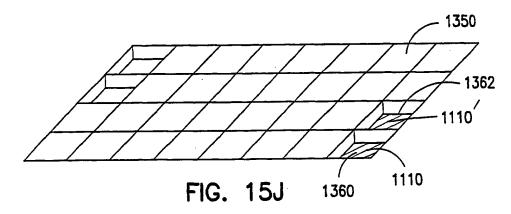
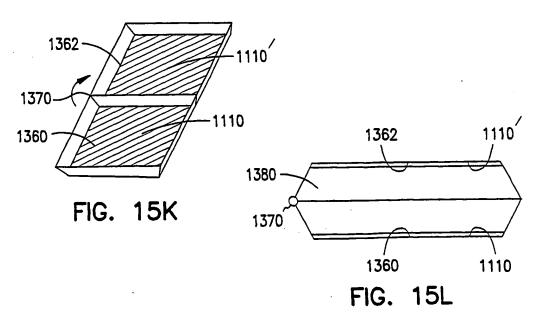
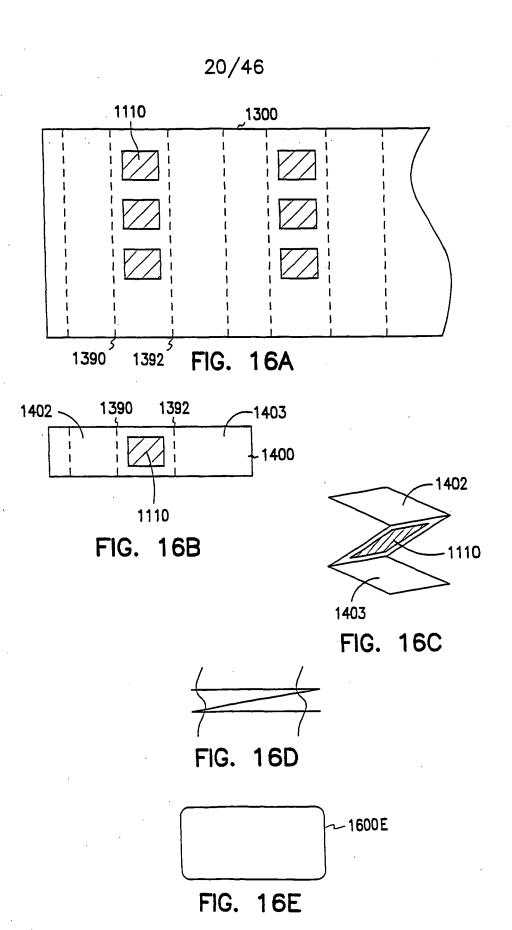


FIG. 151

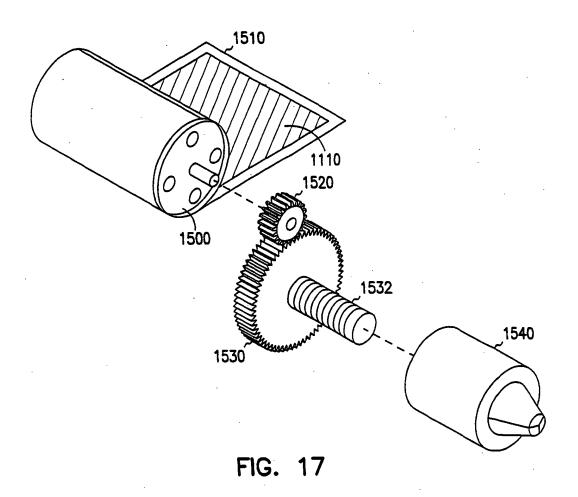




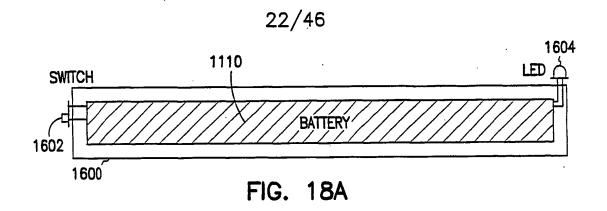


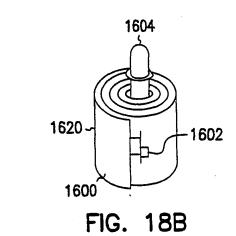


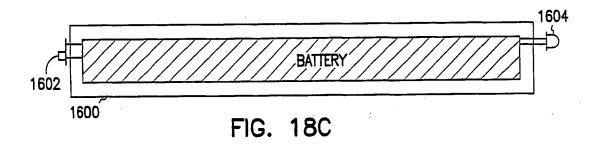


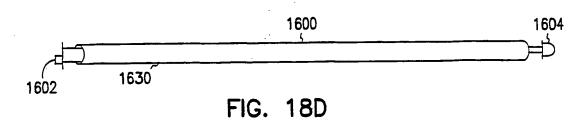






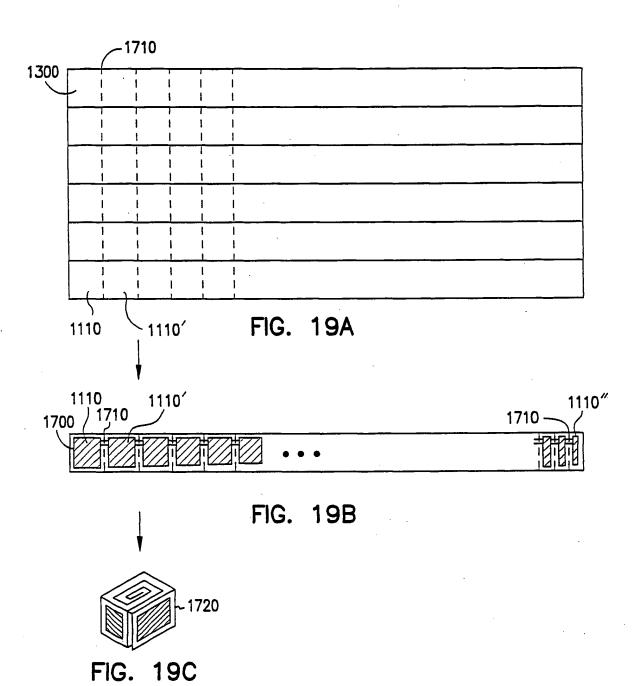




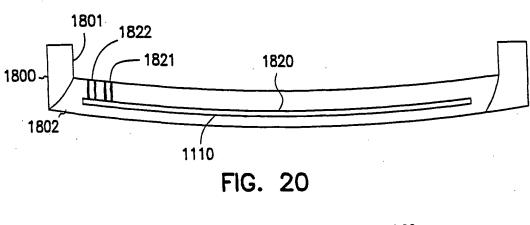




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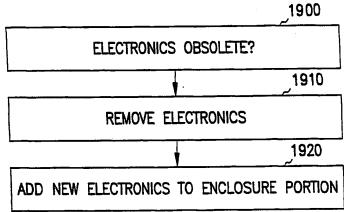


FIG. 21A

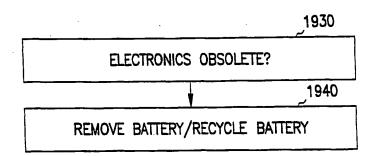
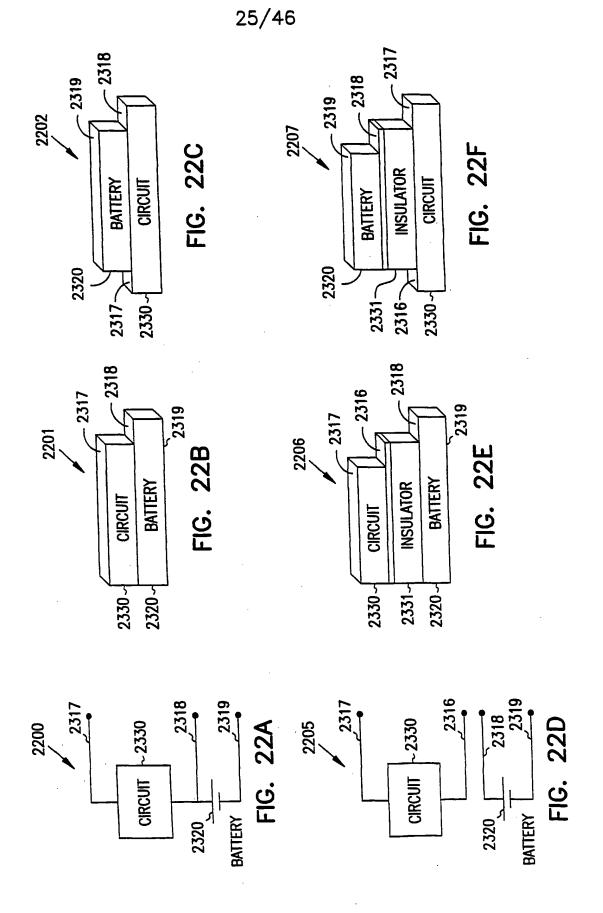
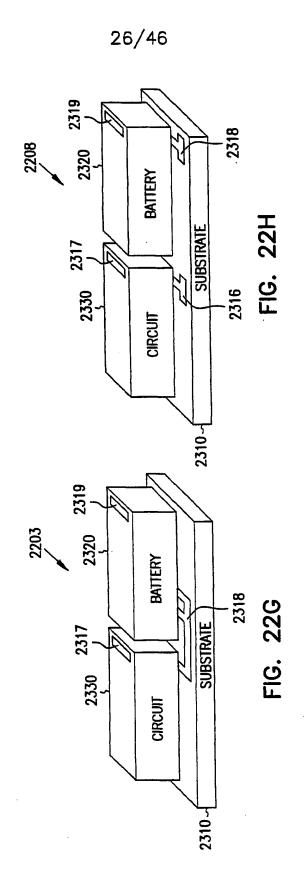


FIG. 21B



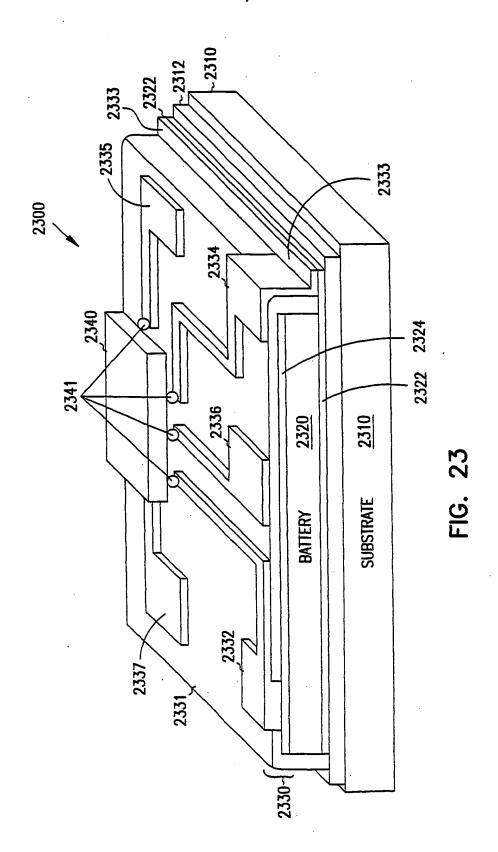












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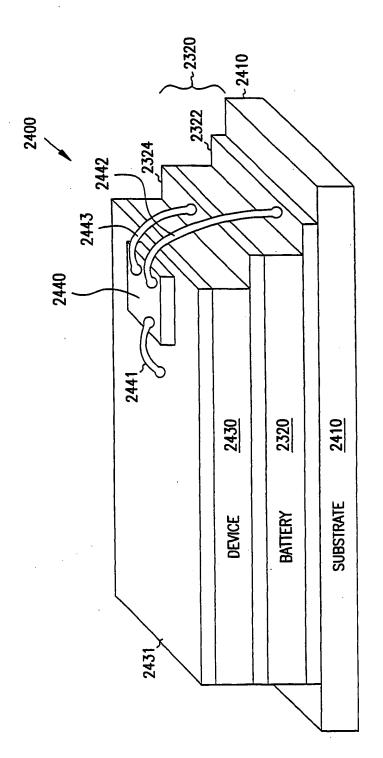
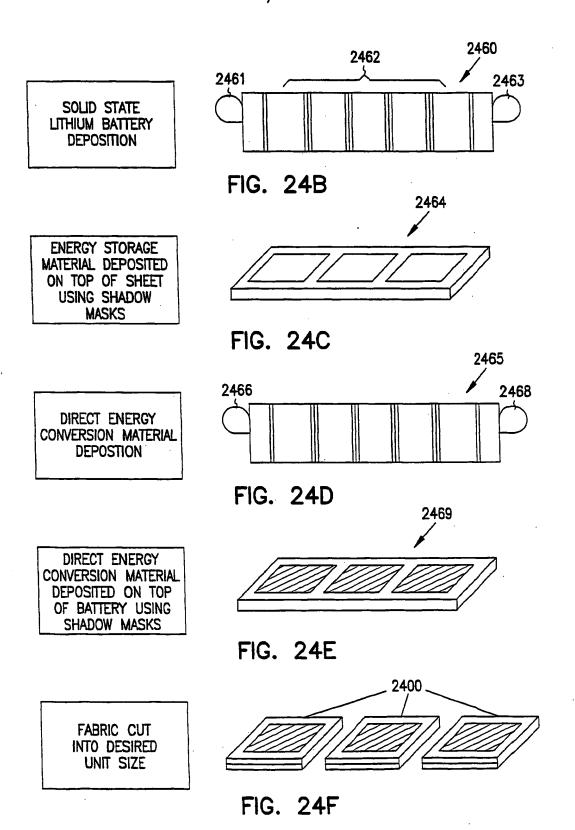
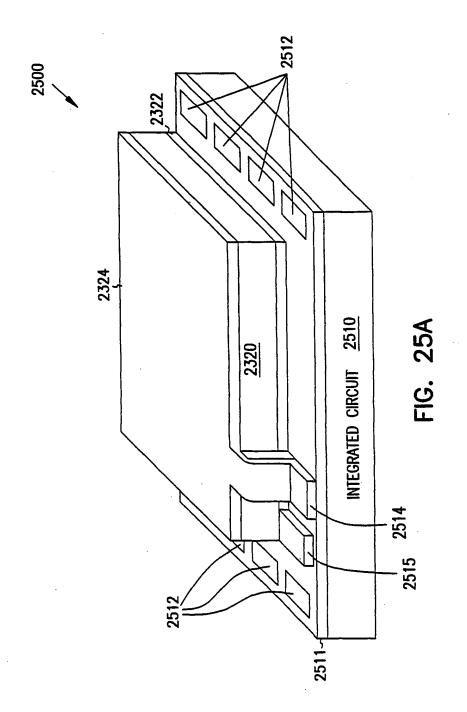


FIG. 24A



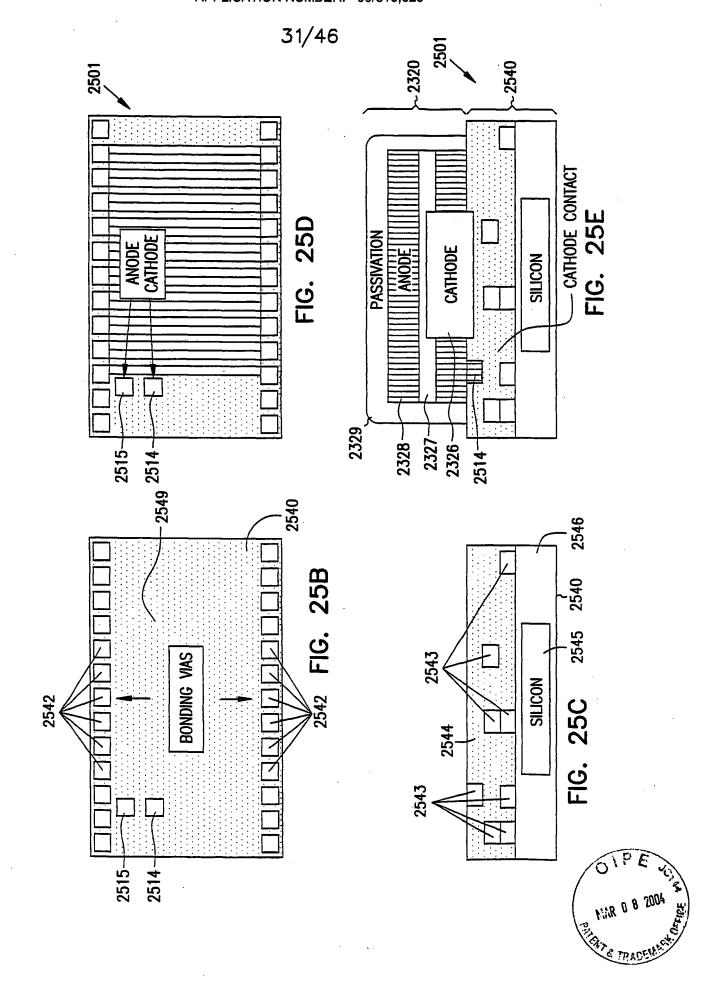








TITLE: METHOD AND APPARATUS FOR INTEGRATED BATTERY DEVICES INVENTOR: MARK L. JENSON ET AL. APPLICATION NUMBER: 09/816,628



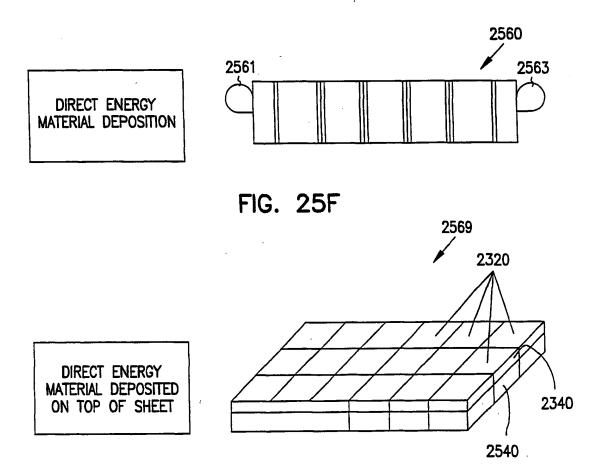


FIG. 25G



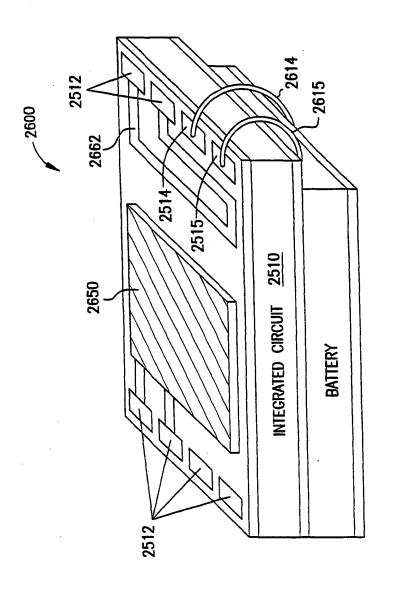
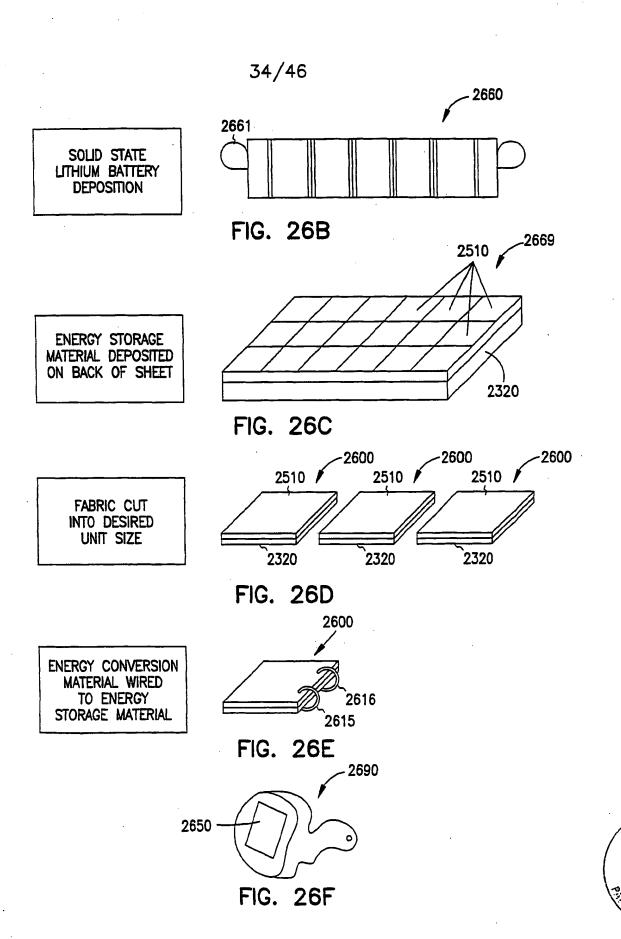


FIG. 26A





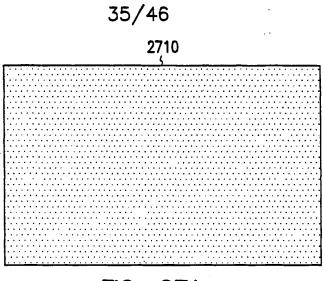
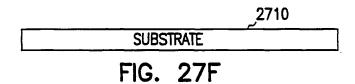
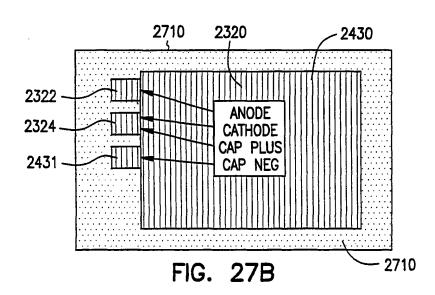


FIG. 27A





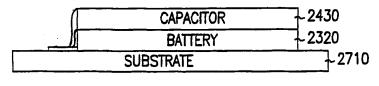
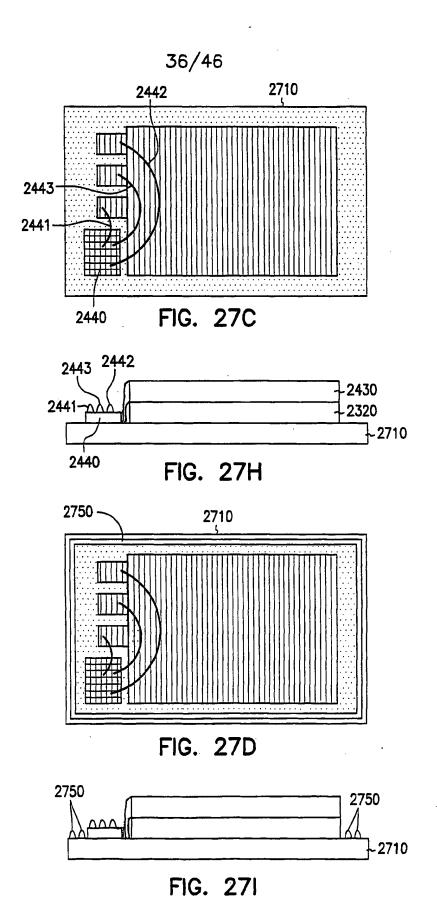


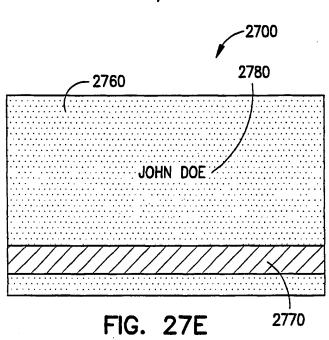
FIG. 27G











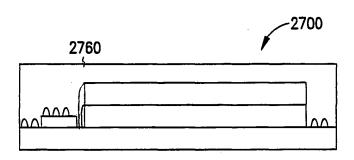


FIG. 27J



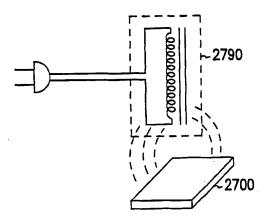


FIG. 27K

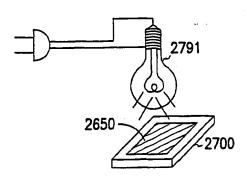


FIG. 27L

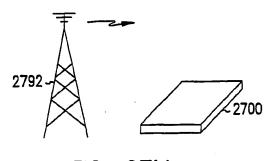
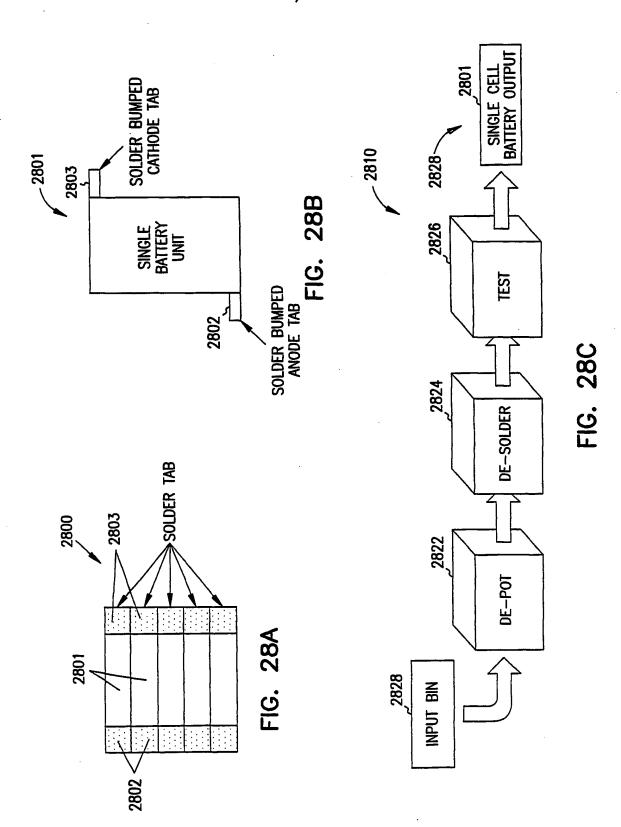


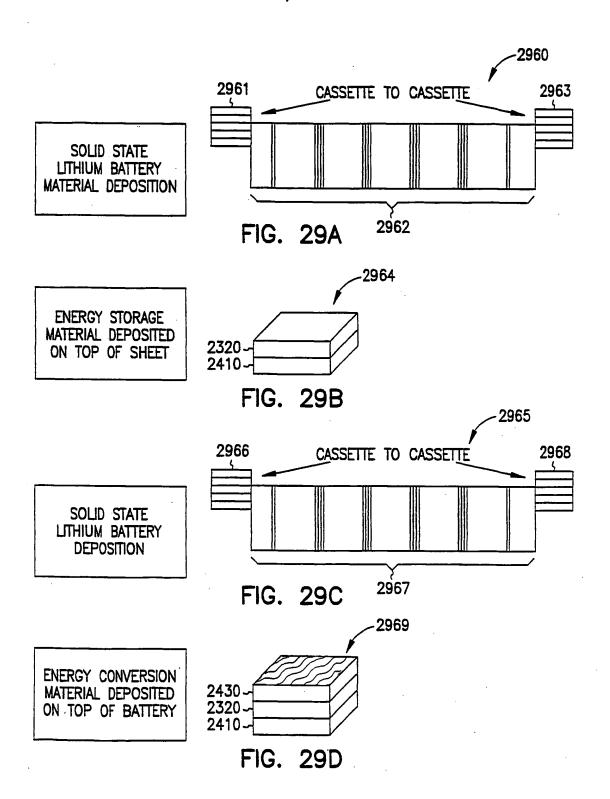
FIG. 27M



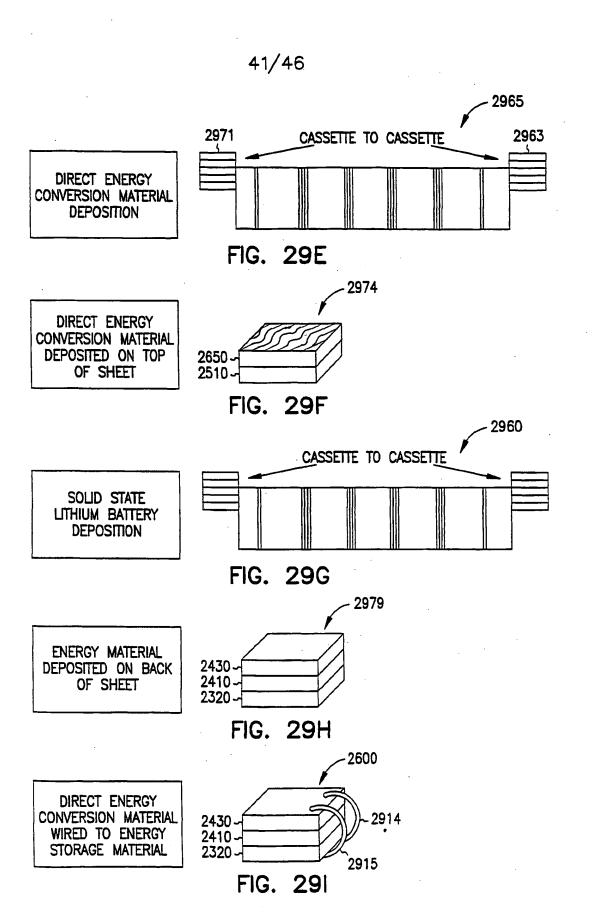














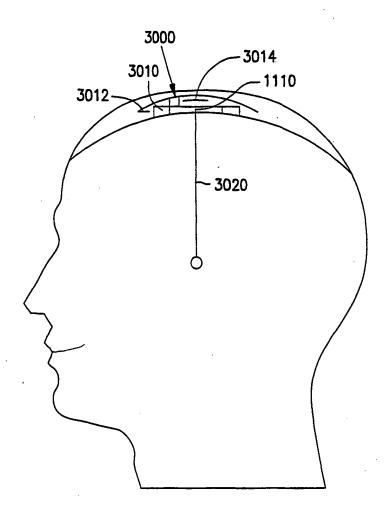


FIG. 30



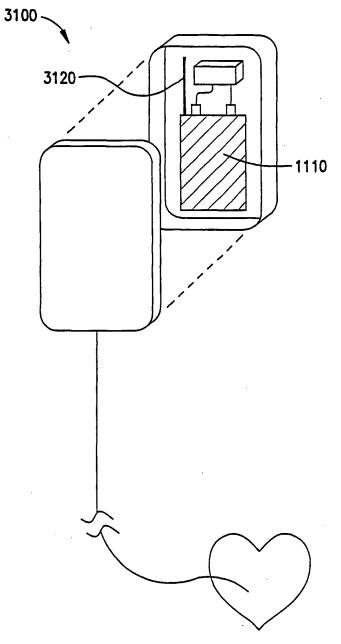
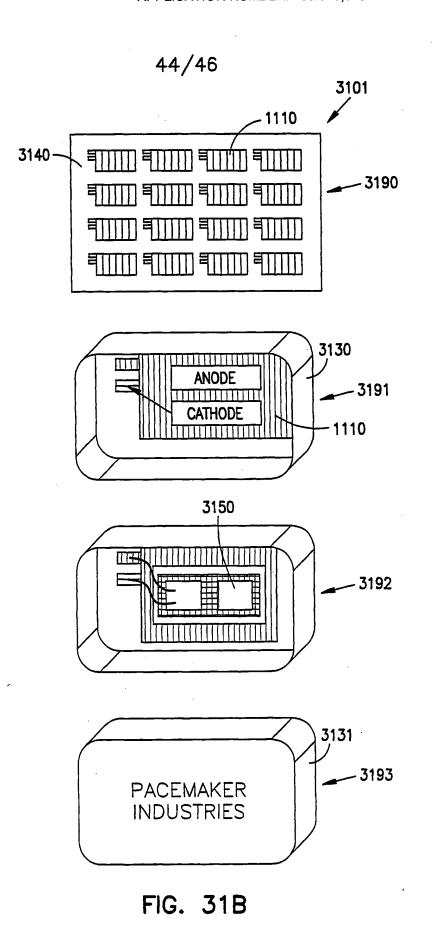
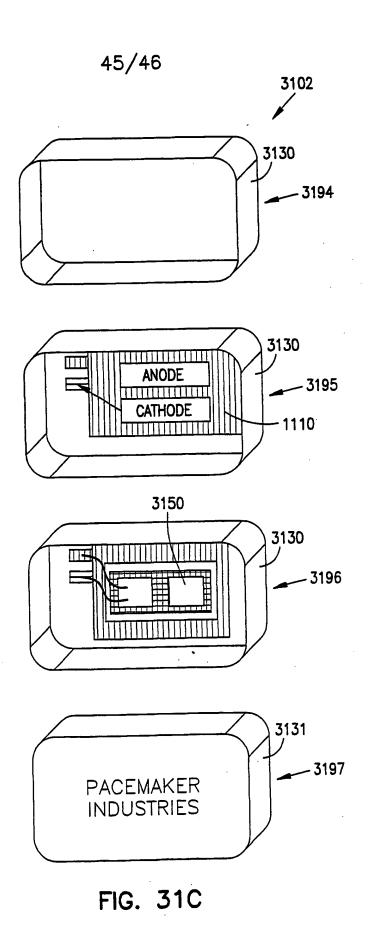


FIG. 31A











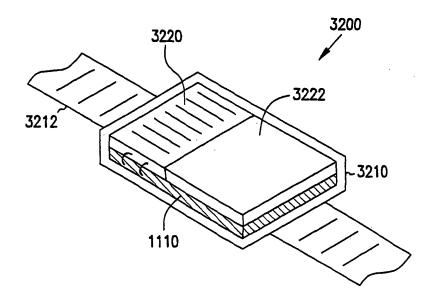


FIG. 32A

